A postdoc or research associate/assistant position in hardware design/development of gateway devices for industrial Wireless Sensor Network (WSN)

Job Summary
A postdoc position is available to develop a wireless-based remote monitoring system. This position will focus on hardware design and development of gateway devices for this monitoring system; and integration of different communication modules in a unit H/W module.

Area of Research
Hardware and circuit design for wireless-based communication system with multiple interfaces (e.g., wireless sensor network, cellular, and satellite); Circuit design and analysis to integrate multiple hardware modules.

Responsibilities and Duties
- Hardware design and development of wireless network gateway device;
- Circuit design, analysis, and implementation for integration of various communication modules in a gateway device
- Prototype development
- Supervision of graduate students working on the same project

Qualifications and Skills
- Ph.D. degree in electrical engineering, computer engineering, or a related field
- Experience with analog and digital circuit design, H/W design, construction, implementation and debugging
- Hands on experience with embedded system development including software implementation
- Good understanding of wireless communication systems

Job Type: Full-time
The position is a one-year Fellowship with the possibility of renewal. Interested candidates are requested to send electronic versions of their curriculum vitae, along with a statement of research interests to Dr. Jin Jiang at jjiang@eng.uwo.ca.

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